TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC74HC4078AP, TC74HC4078AF

TC74HC4078AP

TC74HC4078AF

Weight

DIP14-P-300-2.54

SOP14-P-300-1.27A

DIP14-P-300-2.54

RAHAR

SOP14-P-300-1.27A

: 0.96 g (typ.)

: 0.18 g (typ.)

8-Input OR/NOR Gate

The TC74HC4078A is a high speed CMOS 8-INPUT NOR GATE fabricated with silicon gate C^2MOS technology.

It achieves the high speed operation similar to equivalent LSTTL while maintaining the CMOS low power dissipation.

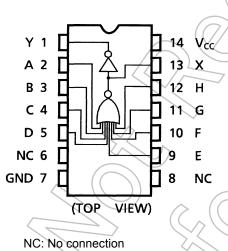
Output X is an 8-INPUT NOR, output Y is an 8-INPUT OR. Each output has a buffer, which provide high noise immunity and stable output.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

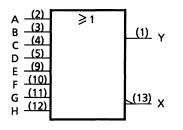
Features

- High speed: $t_{pd} = 13 \text{ ns}$ (typ.) at $V_{CC} = 5 \text{ V}$
- Low power dissipation: $I_{CC} = 1 \ \mu A \ (max)$ at $Ta = 25^{\circ}C$
- High noise immunity: $V_{NIH} = V_{NIL} = 28\% V_{CC} (min)^4$
- Output drive capability: 10 LSTTL loads
- Symmetrical output impedance: $|I_{OH}| = I_{OL} = 4 \text{ mA} (min)$
- Balanced propagation delays: $t_{pLH} \simeq t_{pHL}$
- Wide operating voltage range: V_{CC} (opr) = 2 to 6 V
- Pin and function compatible with 4078B (

Pin Assignment



IEC Logic Symbol



Start of commercial production 1988-11

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Truth Table

Inputs A Through H	Outputs				
Inputs A Through Th	Х	Y			
All Inputs L	Н	L			
All Other Combination	L	Н			

Absolute Maximum Ratings (Note 1)

	iigs (Note I)			Y
Characteristics	Symbol	Rating	Unit	
Supply voltage range	V _{CC}	–0.5 to 7	<u> </u>	
DC input voltage	V _{IN}	–0.5 to V _{CC} + 0.5	> v	
DC output voltage	V _{OUT}	–0.5 to V _{CC} + 0.5	V	\frown
Input diode current	lік	±20	mA	$\langle \frown \rangle$
Output diode current	I _{ОК}	±20	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} /ground current	ICC	±50	mA	$\langle 0 \rangle$
Power dissipation	PD	500 (DIP) (Note 2)/180 (SOP)	mW	
Storage temperature	T _{stg}	-65 to 150	~~~)	

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C shall be applied until 300 mW.

Operating Ranges (Note)

Characteristics	Symbol	Rating	Unit
Supply voltage	Vcc	2 to 6	V
Input voltage	VIN	0 to V _{CC}	V
Output voltage	Vout	0 to V _{CC}	V
Operating temperature	Topr	-40 to 85	°C
		0 to 1000 ($V_{CC} = 2.0 \text{ V}$)	
Input rise and fall time	t _r , t _f	0 to 500 (V _{CC} = 4.5 V)	ns
\sim	*	0 to 400 (V_{CC} = 6.0 V)	

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{CC} or GND.

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			Ta = 25°C		2	Ta = -40 to 85°C		Unit
				$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
					1.50	_ <	X	1.50	_	
High-level input voltage	VIH	—		4.5	3.15	—	$\langle \rangle$	3.15	—	V
, j				6.0	4.20		Ł	4.20		
				2.0	—	10	0.50	_	0.50	
Low-level input voltage	VIL	—		4.5		747	1.35	—	1.35	V
_				6.0		Δ	1.80	—	1.80	
		V _{IN} = V _{IH} or V _{IL}		2.0	1.9	2.0	> _	1.9	—	
			$I_{OH} = -20 \ \mu A$	4.5	4.4	4.5	—	4.4	_	
High-level output voltage	V _{OH}			6.0 <	5.9	6.0	—	5.9	\rightarrow	V
C C			I _{OH} = -4 mA	4.5	4.18	4.31		4.13	> -	
			$I_{OH} = -5.2 \text{ mA}$	6.0	5.68	5.80	-((5.63	<u> </u>	
		V _{IN} = V _{IH} or V _{IL}		2.0		0.0	0.	Y)	0.1	
			I _{OL} = 20 μA	4.5	—	0.0	⊇0.1	\geq	0.1	
Low-level output voltage	V _{OL}		40	6.0	—	0.0	0.1)	—	0.1	V
C C			I _{OL} = 4 mA	4.5		0.17	0.26	_	0.33	
			I _{OL} = 5.2 mA	6.0	_	0,18	0.26	—	0.33	
Input leakage current	IIN	$V_{IN} = V_{CC}$ or	GND	6.0	_	$\overline{)}$	±0.1	_	±1.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{CC}$ or	GND	6.0	\searrow	/_	1.0	—	10.0	μA

AC Characteristics ($C_L = 15 \text{ pF}$, $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}\text{C}$, input: $t_r = t_f = 6 \text{ ns}$)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time			_	4	8	ns
Propagation delay time	t _{pLH} t _{pHL}		_	13	22	ns

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AC Characteristics (C_L = 50 pF, input: $t_r = t_f = 6$ ns)

Characteristics Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	,		$V_{CC}(V)$	Min	Тур.	Max	Min	Max	
	4		2.0	_	30	75	_	95	
Output transition time	t _{TLH}	—	4.5	—	8	15	—	19	ns
t _{THL}	ITHL		6.0	—	7	13	—	16	
	4		2.0	_	50	130	2	165	
Propagation delay ^t pLH time t _{pHL}	•	—	4.5	—	16	26	Ŋ	33	ns
	^τ pHL		6.0	_	14	22	_	28	
Input capacitance	C _{IN}	_		-	5	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)	_((40	> —	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

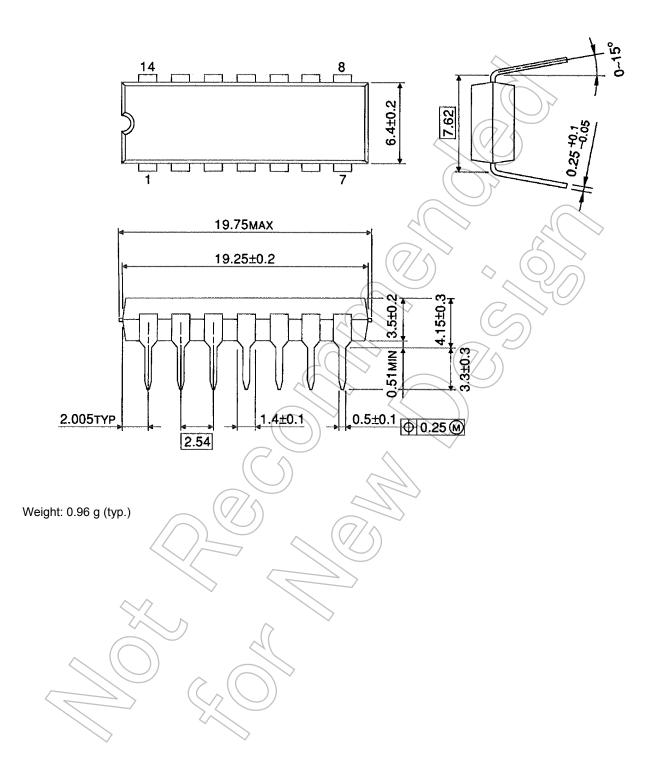
Average operating current can be obtained by the equation?

 $I_{CC} \text{ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

DIP14-P-300-2.54

Unit : mm

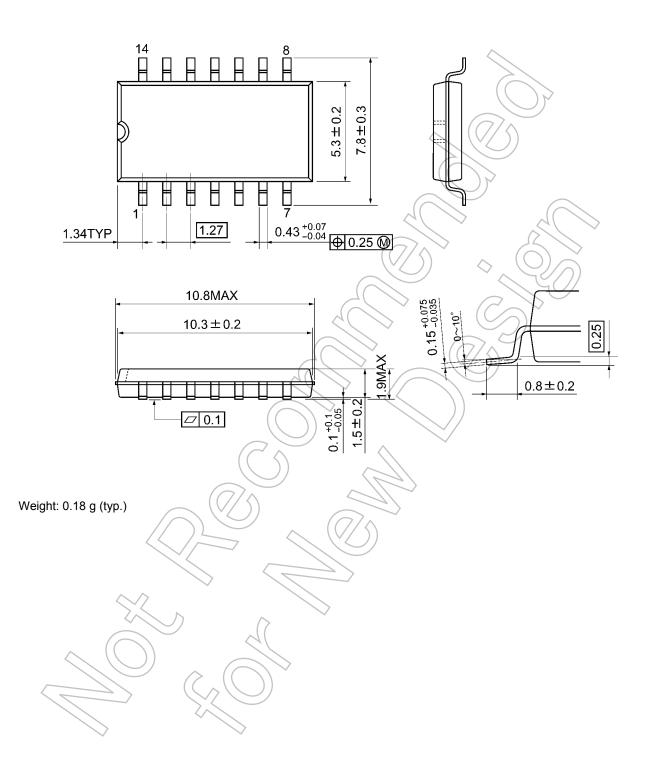




Package Dimensions

SOP14-P-300-1.27A

Unit: mm



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